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(71) **APPLICANT:** DAEWOO ELECTRONICS COOPERATION  
Soon-Hoon BAE  
Namdaemoonro 1 ga, 541, Jung-gu, Seoul, Republic of Korea

(72) **INVENTOR:** Young-Sang KIM  
Wansungbila #La-304, Myunmok 8 dong, Jungrang-gu,  
Republic of Korea

(74) **AGENT:** Cheon-Woong JIN

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(54) **TITLE OF INVENTION:** ADDRESS GENERATOR FOR COEFFICIENT MEMORY  
OF OFDM CHANNEL EQUALIZER

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**[ABSTRACT]**

The present invention relates to an Orthogonal Frequency Division Multiplexing(OFDM) channel equalizer, in particular, to an address generator for access to coefficient memory in an adaptive channel equalizer according to the changes of channels. The present invention is comprised of a pulse generator 90 for generating a pulse(SPC(t)) by sensing the location of a pilot signal inserted into the symbol after receiving a symbol ID, a writing counter 91 for being reset by symbol sync and generating a write address after receiving the pulse provided by the aforesaid pulse generator 90 in the form of CLK, a distributor 92 for generating a pulse distributed after receiving a sample CLK, and a reading counter 93 for being reset by symbol sync and generating a read address after receiving a output pulse in the aforesaid distributor 92 in the form of a CLK. The present invention generates a memory address and control signal through a simple consecutive circuit using a counter and the like so as to make it efficient to access a coefficient renewed according to the pilot insertion rules.

**[REPRESENTATIVE DRAWING]**

Fig. 9